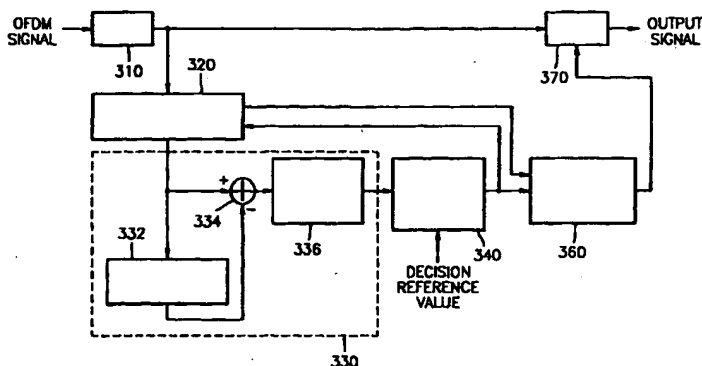




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(21) International Application Number: PCT/KR98/00299 (22) International Filing Date: 28 September 1998 (28.09.98) (30) Priority Data: 1997-50377 30 September 1997 (30.09.97) KR (71) Applicant (for all designated States except US): SAMSUNG ELECTRONICS CO., LTD. [KR/KR]; 416, Maetan-dong, Paldal-gu, Suwon-city, Kyungki-do 442-373 (KR). (72) Inventors; and (75) Inventors/Applicants (for US only): DO, Sang, Hyun [KR/KR]; 27-611, Jangmi Apt., 11, Shinchun-dong, Songpa-gu, Seoul 138-240 (KR). KIM, Dong, Kyu [KR/KR]; 1055-66, Kwangan 1-dong, Sooyoung-gu, Pusan-city 613-101 (KR). CHOI, Hyung, Jin [KR/KR]; 10-405, Asia Sunsoochon Apt., 86, Chamsil-dong, Songpa-gu, Seoul 138-220 (KR). (74) Agent: LEE, Young, Pil; The Cheonghwa Building, 1571-18, Seocho-dong, Seocho-gu, Seoul 137-073 (KR).		(81) Designated States: AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GE, GH, GM, HR, HU, ID, IL, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG). Published With international search report. Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.

(54) Title: **GUARD SECTION LENGTH DETECTING DEVICE AND METHOD FOR ORTHOGONAL FREQUENCY DIVISION MULTIPLEXING SYSTEM RECEIVER**



(57) Abstract

An apparatus for detecting a guard interval length (type) of a transmission symbol among the structure of an orthogonal frequency division multiplexing (OFDM) system receiver, to ensure proper operation of a symbol start detector (320) and a fast Fourier transform (FFT) window position controller (360), is provided. This apparatus includes an analog-to-digital converter (ADC) (310) for converting a received OFDM signal into digital complex samples, a symbol start position detector (320) for detecting a symbol start position with reference to information on a guard interval length among the complex samples output by the ADC (310), a symbol start position difference detector (330) for detecting the difference between the symbol start position detected by the symbol start position detector (320) and a symbol start position delayed for a predetermined symbol time, a guard interval length detector (340) for comparing the symbol start position difference detected by the symbol start position difference detector (330) with a predetermined guard interval decision reference value and detecting the length of the guard interval, and an FFT window position controller (360) for shifting the FFT window position using the guard interval length detected by the guard interval length detector (340) and the symbol start information detected by the symbol start detector (320), to activate an FFT at the shifted FFT window position. The lengths of various guard intervals are automatically detected in the OFDM receiver, thus performing accurate FFT window recovery.

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**GUARD SECTION LENGTH DETECTING DEVICE AND METHOD
FOR ORTHOGONAL FREQUENCY DIVISION MULTIPLEXING SYSTEM
RECEIVER**

5 Technical Field

The present invention relates to an orthogonal frequency division multiplexing (OFDM) system, and more particularly, to a device and method for detecting a guard interval length in an OFDM receiver.

10 Background Art

FIG. 1 is a block diagram of a receiver in a general OFDM system, including an analog-to-digital converter (ADC) 110, a symbol start detector 120, a fast Fourier transform (FFT) window position controller 130, and a fast Fourier transformer (FFT) 140.

15 First, considering a symbol of an OFDM signal, when N subcarriers are used in an OFDM system, the symbol is comprised of N useful data samples as the output of an inverse fast Fourier transform (IFFT) for transmission, and a guard interval having G sample lengths to be inserted before a useful data section to prevent interference between symbols.

20 Here, the guard interval copies the end portion of the useful data section. A transmitter (not shown) adds G complex values to N complex values output by an inverse fast Fourier transformer (IFFT), and sequentially transmits a symbol comprised of a total of (G+N) samples.

Here, the guard interval is generally longer than a delay spread time
25 of a channel. For example, a European digital TV broadcasting standard defines a guard interval having a length of 1/4, 1/8, 1/16 or 1/32 (hereinafter, called a 1/4 mode, a 1/8 mode, a 1/16 mode, and a 1/32 mode) of an actual symbol length. A transmitting side selects a length among these and uses the selected length. A receiver must perform accurate time synchronization
30 to recover a received OFDM signal. The time synchronization is comprised of FFT window position recovery for parallel processing of an accurate signal, and sampling clock recovery for controlling a sampling clock of a

received signal having a maximum signal-noise-ratio (SNR).

$$S_j = \sum_{n=-G}^{N-1} X_{j,n} = \sum_{n=-G}^{-1} \sum_{k=0}^{N-1} X_{j,k} e^{j2\pi(N+n)/N} + \sum_{n=0}^{N-1} \sum_{k=0}^{N-1} X_{j,k} e^{j2\pi kn/N} \quad \dots(1)$$

Equation 1 expresses a j-th symbol comprised of a useful interval and a guard interval which are output by an IFFT (not shown) of a transmitter. Here, j denotes a symbol number, k is a carrier index, N is the number of useful data samples, n indicates a sampling time, and $X(\bullet)$ and $x(\bullet)$ respectively denote an input complex value and an output complex value of the transmission IFFT. In the right side of Equation 1, the first term is a guard interval portion and the second term is a useful data portion.

As shown in FIG. 1, the ADC 110 samples a received OFDM signal. The symbol start detector 120 detects information on a start portion of a symbol using the type of guard interval and the sampled OFDM signal. The FFT window controller 130 designates an FFT window point in time to activate the useful data portion of the FFT 140 using length information on the guard interval and length information of the symbol start portion detected by the symbol start detector 120. However, a device for detecting information on the length of the guard interval has been developed up to now, and thus correct operation of the symbol start detector 120 and the FFT window controller 130 cannot be ensured.

20 Disclosure of the Invention

It is an objective of the present invention to provide a method of detecting the length of a guard interval, whereby accurate FFT window recovery is performed by automatically searching for the length of the guard interval by calculating the difference between symbol start points in time in an OFDM receiver.

It is an objective of the present invention to provide a device for detecting the length of a guard interval, whereby accurate FFT window recovery is performed by automatically searching for the length of the guard interval by calculating the difference between symbol start points in time in

an OFDM receiver.

To achieve the first objective, there is provided a method of detecting the type of guard interval to recover a symbol start position in an orthogonal frequency division multiplexing (OFDM) receiver for receiving an OFDM symbol comprised of a useful data interval and a guard interval, the method comprising the steps of: (a) detecting the difference between a symbol start position of a received OFDM signal and a symbol start position delayed during at least two symbols; and (b) detecting the length of the guard interval by comparing the symbol start position difference detected by the step (a) to a predetermined guard interval decision reference value.

To achieve the second objective, there is provided an apparatus for detecting the type of guard interval in an orthogonal frequency division multiplexing (OFDM) receiver for receiving an OFDM symbol comprised of useful data interval and a guard interval, to recover a fast Fourier transform (FFT) window position, the apparatus comprising: an analog-to-digital converter (ADC) for converting a received OFDM signal into digital complex samples; a symbol start position detector for detecting a symbol start position with reference to information on a guard interval length among the complex samples output by the ADC; a symbol start position difference detector for detecting the difference between the symbol start position detected by the symbol start position detector and a symbol start position delayed for a predetermined symbol time; a guard interval length detector for comparing the symbol start position difference detected by the symbol start position difference detector with a predetermined guard interval decision reference value and detecting the length of the guard interval; and an FFT window position controller for shifting the FFT window position using the guard interval length detected by the guard interval length detector and the symbol start information detected by the symbol start detector, to activate an FFT at the shifted FFT window position, wherein the symbol start position difference detector comprises: a symbol delay for delaying by two symbols the symbol start position detected by the symbol start position detector; and a subtractor for calculating the difference between the symbol start position

detected by the symbol start position detector and the symbol start position output by the symbol delay.

Brief Description of the Drawings

5 FIG. 1 is a block diagram illustrating the configuration of a general OFDM system receiver;

FIG. 2 is a block diagram illustrating an FFT window position recovering apparatus in an OFDM system receiver according to the present invention; and

10 FIG. 3 is a timing view illustrating the transition of a symbol start position according to a guard interval mode (1/32 mode, 1/16 mode, 1/8 mode, or 1/4 mode).

Best mode for carrying out the Invention

15 Referring to FIG. 2, an FFT window position recovering apparatus includes an ADC 310, a symbol start position detector 320, a symbol start position difference detector 330, a guard interval length detector 340, an FFT window position controller 360, and an FFT 370. The symbol start position difference detector 330 is comprised of a symbol delay 332, a subtractor 334
20 and an absolute value calculator 336.

Referring to FIG. 3, a detection period corresponds to the length of an OFDM symbol in a 1/32 mode, and is repeated in units of 2112 samples being the sum of a guard interval and a useful data interval. The symbol start position detector 320 detects a symbol start position in this detection
25 period. A greyed portion in the detection period is a guard interval, and the remaining portion is a useful data interval.

The operation and effects of the present invention will now be described referring to FIGS. 2 and 3.

30 As shown in FIG. 2, the ADC 310 samples an input OFDM signal and converts the sampled OFDM signal into digital data as expressed by the following Equation 2:

$$y(k) = y(t), t = kT \quad \dots(2)$$

wherein $y(k)$ is a sampled complex signal, $y(t)$ is a received OFDM signal, k is an integer (0, 1, 2, ...), and T is a regular sample period of the ADC 310.

The symbol start position detector 320 detects a symbol starting portion using a method of finding a maximum position of a correlation value between received complex signals $y(k)$ or finding a point in time where the correlation value between quantized received signal values is maximum. Also, the symbol start position detector 320 detects the symbol start position when the length of a guard interval is set to a minimum length (a 1/32 mode in this embodiment). Here, the position of an output obtained by consecutively detecting the guard interval length is shifted at a regular interval in cases other than when the actual length of the guard interval is the 1/32 mode. That is, as shown in FIG. 3, when the guard interval is the 1/32 mode, no position shift occurs between symbol start positions (between a previous symbol start position and the next symbol start position). When the guard interval is the 1/16 mode, the position of the guard interval is shifted by 1/32 of the useful data interval between the symbol start positions (between a previous symbol start position and the next symbol start position). When the guard interval is the 1/8 mode, the position of the guard interval is shifted by 3/32 of the useful data interval between the symbol start positions (between a previous symbol start position and the next symbol start position). When the guard interval is the 1/4 mode, the position of the guard interval is shifted by 7/32 of the useful data interval between the symbol start positions (between a previous symbol start position and the next symbol start position). However, in an actual receiving environment, additive white gaussian noise (AWGN), multi-path channel distortion, etc. have influence, so that the position shift between adjacent symbols is made at inaccurate intervals, and the distribution of position shift intervals capable of occurring according to the mode of each guard interval overlaps significantly. Therefore, the accurate length of guard interval cannot be detected.

Accordingly, the symbol start position difference detector 330 detects the difference between a current symbol start position detected by the

symbol start position detector 320 and a symbol start position delayed for at least 2 symbols. Here, the symbol delay 332 delays the symbol start position detected by the symbol start position detector 320 for a 2 symbol period in a best mode, to reliably detect the difference between the symbol start positions under the influence of noise and so on.

The subtractor 334 obtains the difference between the symbol start position output by the symbol delay 332 and the symbol start position output by the symbol start position detector 320. The symbol start position difference value output by the subtractor 334 is a position shift value at the symbol start position. The present invention uses only the size of the difference between the symbol start positions, so the absolute value calculator 336 calculates the absolute value of the output of the subtractor. The guard interval length detector 340 compares the symbol start position difference detected by the symbol start position difference detector 330 with a decision reference value prestored in a storage means such as a ROM, etc., and detects the length of a corresponding guard interval. Here, the decision reference value, used to decide the mode of the guard interval length, is set through an experiment. For example, the guard interval length detector 340 detects a guard interval length of a 1/32 mode when the difference between two symbol-delayed symbol start positions (between a previous symbol start position and the next symbol start position) is less than 20 samples (a 1/32 mode portion of FIG. 3). The guard interval length detector 340 detects a guard interval length of a 1/16 mode when the difference (distance) between the symbol start positions is between 100 samples and 150 samples (a 1/16 mode portion of FIG. 3). The guard interval length detector 340 detects a guard interval length of a 1/8 mode when the difference (distance) between the symbol start positions is between 350 samples and 400 samples (a 1/8 mode portion of FIG. 3). The guard interval length detector 340 detects a guard interval length of a 1/8 mode when the difference between the symbol start positions is less than 1000 samples (a 1/4 mode portion of FIG. 3).

Information on the guard interval length of the guard interval length

detector 340 is output to the FFT window position controller 360, and simultaneously fed back to the symbol start position detector 320. The symbol start position detector 320 converts the existing guard interval length into a guard interval length newly input from the guard interval length
5 detector 340, detects a symbol start position using the converted guard interval length information, and outputs the resultant value to the FFT window position controller 360.

Thus, the FFT window position controller 360 transfers an FFT window position using the guard interval length detected by the guard
10 interval length detector 340 and the symbol start position information input from the symbol start position detector 320, and activates the fast Fourier transform (FFT), selecting only the useful data portion. The FFT 370 performs FFT on data generated by the ADC 310 according to an FFT window position control signal generated by the FFT window position
15 controller 360, and outputs an OFDM signal which has been fast-Fourier-transformed.

Industrial Applicability

According to the present invention as described above, the lengths of
20 various guard intervals are automatically detected in an OFDM receiver, thus performing accurate FFT window recovery.

What is claimed is:

1. A method of detecting the type of guard interval to recover a symbol start position in an orthogonal frequency division multiplexing (OFDM) receiver for receiving an OFDM symbol comprised of a useful data interval and a guard interval, the method comprising the steps of:

(a) detecting the difference between a symbol start position of a received OFDM signal and a symbol start position delayed during at least two symbols; and

(b) detecting the length of the guard interval by comparing the symbol start position difference detected by the step (a) to a predetermined guard interval decision reference value.

2. An apparatus for detecting the type of guard interval in an orthogonal frequency division multiplexing (OFDM) receiver for receiving an OFDM symbol comprised of useful data interval and a guard interval, to recover a fast Fourier transform (FFT) window position, the apparatus comprising:

an analog-to-digital converter (ADC) for converting a received OFDM signal into digital complex samples;

a symbol start position detector for detecting a symbol start position with reference to information on a guard interval length among the complex samples output by the ADC;

a symbol start position difference detector for detecting the difference between the symbol start position detected by the symbol start position detector and a symbol start position delayed for a predetermined symbol time;

a guard interval length detector for comparing the symbol start position difference detected by the symbol start position difference detector with a predetermined guard interval decision reference value and detecting the length of the guard interval; and

an FFT window position controller for shifting the FFT window position using the guard interval length detected by the guard interval length detector

and the symbol start information detected by the symbol start detector, to activate an FFT at the shifted FFT window position.

3. The apparatus for detecting the type of guard interval in an
5 OFDM receiver as claimed in claim 2, wherein the symbol start position difference detector comprises:

a symbol delay for delaying by two symbols the symbol start position detected by the symbol start position detector; and

a subtractor for calculating the difference between the symbol start
10 position detected by the symbol start position detector and the symbol start position output by the symbol delay.

4. The apparatus for detecting the type of guard interval in an
OFDM receiver as claimed in claim 3, wherein the symbol start position
15 difference detector further comprises:

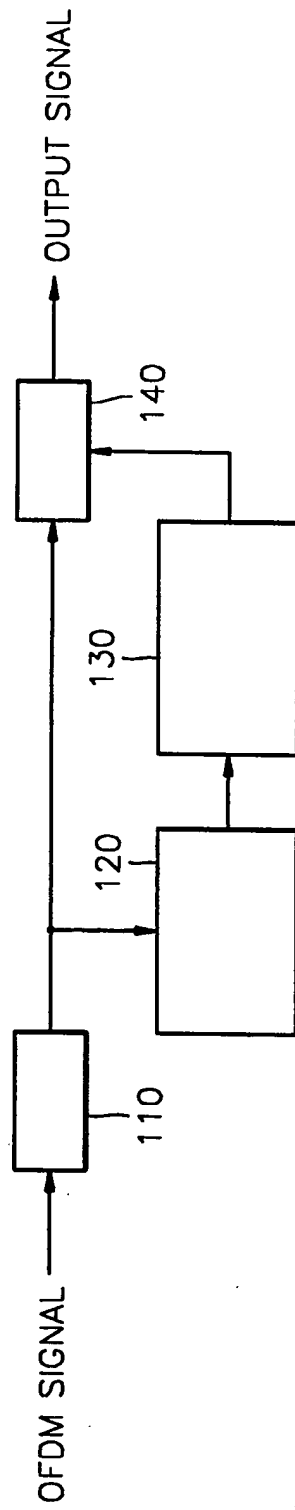
an absolute value calculator for calculating the absolute value of the symbol start position difference output by the subtractor.

5. The apparatus for detecting the type of guard interval in an
20 OFDM receiver as claimed in claim 2, wherein the symbol start position detector detects an initial symbol start position when a guard interval length is set to be a 1/32 mode.

6. The apparatus for detecting the type of guard interval in an
25 OFDM receiver as claimed in claim 2, wherein the symbol start position difference detected by the symbol start position difference detector is a symbol start position shifted value.

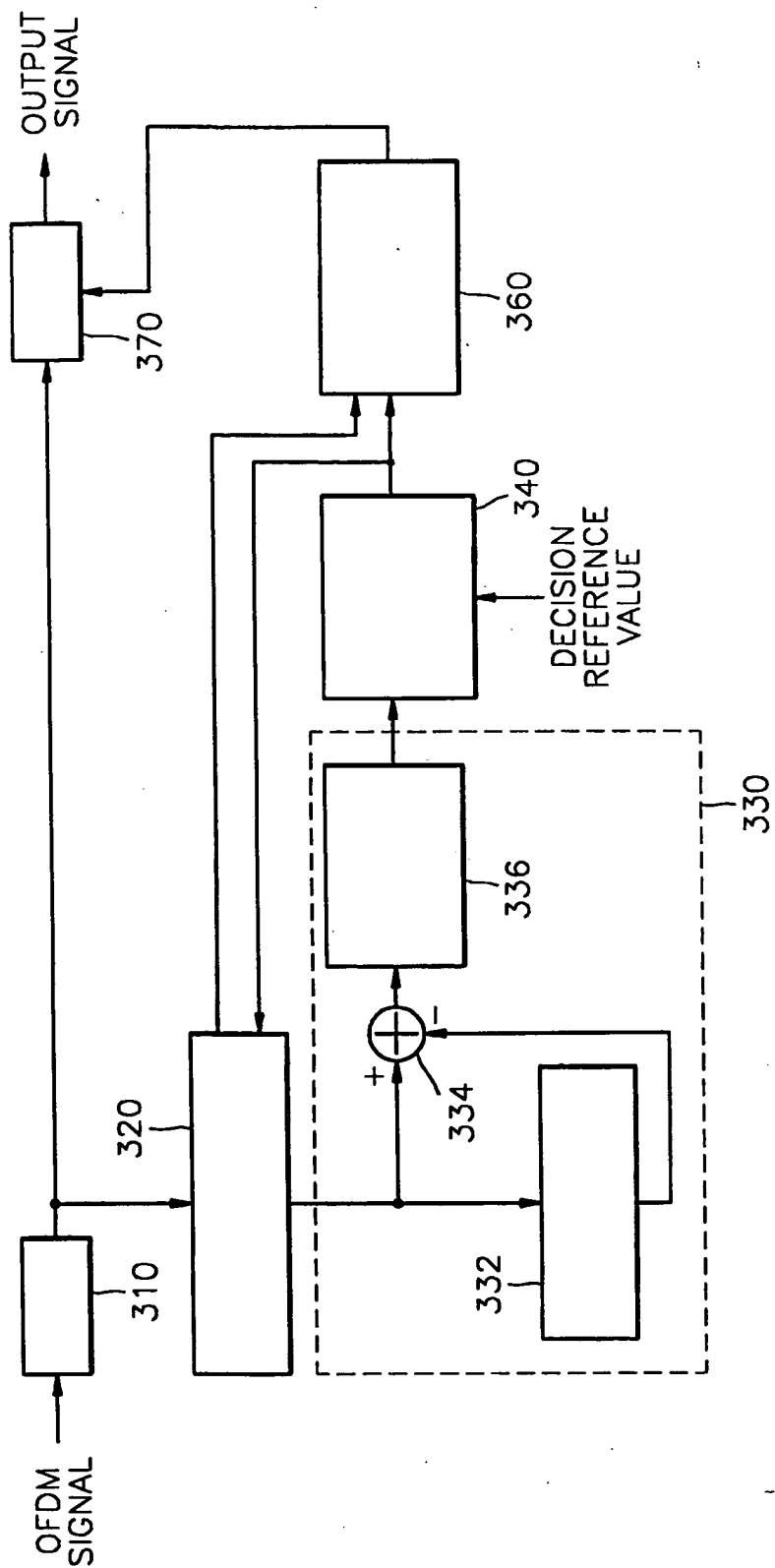
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FIG. 1



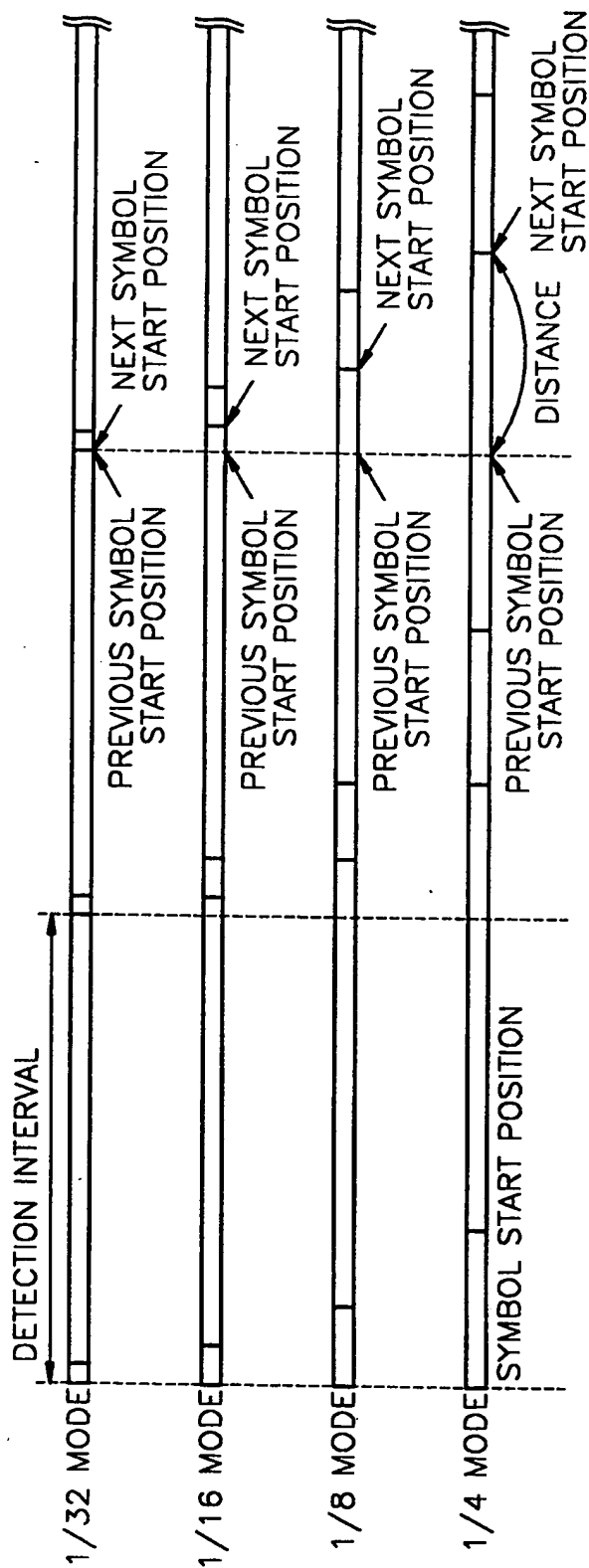
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FIG. 2



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FIG. 3



INTERNATIONAL SEARCH REPORT

International application No.
PCT/KR 98/00299

A. CLASSIFICATION OF SUBJECT MATTER

IPC⁶: H 04 L 5/06; H 04 N 7/06

According to International Patent Classification (IPC) or to both national classification and IPC

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Minimum documentation searched (classification system followed by classification symbols)

IPC⁶: H 04 L 5/06, 7/00, 27/26; H 04 N 7/06

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 0 772 332 A2 (BRITISH BROADCASTING) 07 May 1997 (07.05.97), abstract; fig.11.	1,2
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☐ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

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